100G QSFP28 LR4

Datasheet



Features:

- 4 independent LAN-WDM channels
- Up to 25.78125Gbps data rate per channel
- Duplex LC receptacles
- Hot-pluggable QSFP28 form factor
- Digital diagnostic capabilities
- Maximum link length of 10km on Single Mode Fiber(SMF)
- Single +3.3V power supply
- Operating case temperature: 0~70℃
- Low power consumption < 3.5W
- RoHS-6 compliant

Applications:

100GBASE LR4 100G Ethernet

Product Description

The TQS-HG10-31DCR is a parallel 100Gbps Quad Small Form-factor Pluggable (QSFP) optical module. It provides increased port density and total system cost savings. The QSFP28 full-duplex optical module offers 4 independent transmit and receive channels, each capable of 25.78125Gbps operation for an aggregate data rate of 100Gbps over 10km of single mode fiber.

An optical fiber cable with duplex LC connectors can be plugged into the QSFP28 module receptacle. The module operates by a single +3.3V power supply. LVCMOS/LVTTL global control signals, such as Module Present, Reset, Interrupt and Low Power Mode, are available with the modules. A 2-wire serial interface is available to send and receive more complex control signals, and to receive digital diagnostic

The TQS-HG10-31DCR is designed with form factor, optical/electrical connection and digital diagnostic interface according to the QSFP Multi-Source Agreement (SFF-8436). It has been designed to meet the harshest external operating conditions including temperature, humidity and EMI interference. The module offers very high functionality and feature integration, accessible via a two-wire serial interface.

information.

Ordering information

Part No.	Data	Laser	Fiber	Distance*Note1	Optical	Temp. *Note2	DDMI
	Rate		Туре		Interface		
TQS-HG10-31DCR	100Gbps	DFB-based	SMF	10km	LC	ST	V
		LAN-WDM					Y

Note1: 10km with 9/125 μ m SMF Note2: ST: -5 \sim +70 deg C

Regulatory Compliance

Feature	Standard	Performance
Electrostatic Discharge	MIL-STD-883G	HBM class 1, 1000volts and above,
(ESD) to the	Method 3015.7	Contact discharge on Golden Finger.
Electrical Pins		
Electrostatic Discharge	IEC-61000-4-2	Compliant with standards.
to the enclosure	GR-1089-CORE	
Electromagnetic	FCC Part 15 Class B	Compliant with standards Noise
Interference (EMI)	EN55022:2006	frequency range: 30MHz to 18 GHz.
	VCCI Class B	System margins depend on customer
		host board and chassis design.
Immunity	IEC 61000-4-3	Compliant with standards.
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11	CDRH compliant and Class I laser
	EN (IEC) 60825-1:2007	product.
	EN (IEC) 60825-2:2004+A1	
Component Recognition	UL and CUL	Compliant with standards.
	EN60950-1:2006	
RoHS6	2002/95/EC 4.1&4.2	Compliant with standards*note3
	2005/747/EC 5&7&13	

Note3:

In light of item 5 in RoHS exemption list of RoHS Directive 2002/95/EC, Item 5: Lead in glass of cathode ray tubes, electronic components and fluorescent tubes.

In light of item 13 in RoHS exemption list of RoHS Directive 2005/747/EC, Item 13: Lead and cadmium in optical and filter glass. The three exemptions are being concerned for Trixon transceivers, because Trixon transceivers use glass, which may contain Pb, for components such as lenses, windows, isolators, and other electronic components.

Absolute Maximum Ratings*Note4

Parameter	Symbol	Min	Max	Unit
Storage Temperature	Tst	-40	85	degC
Relative Humidity (non-condensation)	RH	15	85	%
Supply Voltage	VCC	-0.5	3.6	V
Voltage on LVTTL Input	Vilvttl	-0.5	VCC+0.5	V
LVTTL Output Current	Lolvttl	-	15	mA

Note4: Exceeding any one of these values may destroy the device permanently.

Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit	Notes
Operating Case Temperature	Торс	0	-	70	$^{\circ}$	
Power Supply Voltage	VCC	3.135	-	3.465	V	
Fiber Length: SMF		-	-	10	km	

Performance Specifications – Electrical

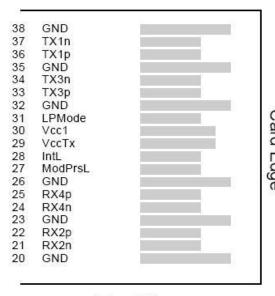
Parameter	Symbol	Min	Тур.	Max	Unit	Notes
Data Rate, each Lane		-	25.78125	-	Gbps	
Power Consumption (XLPPI)		-	-	3.5	W	
Supply Current	ICC	-	-	1.12	Α	
Control I/O Voltage, High	VIH	2.0	-	VCC	V	
Control I/O Voltage, Low	VIL	0	-	0.8	V	
Tx Input Diff Voltage	VI	150	-	1200	mV	
Tx Input Diff Impedance	ZIN	85	100	115	Ω	
Rx Output Diff Voltage	Vo	200		1100	mV	
Rx Output Diff Impedance	ZOUT	85	100	115	Ω	

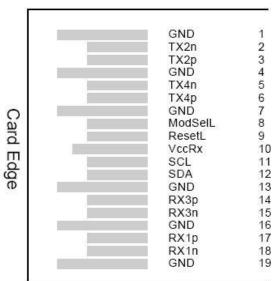
Performance Specifications – Optical

Parameter	Symbol	Min	Тур.	Max	Unit			
Transmitter								
		1294.53		1296.59				
Contar Wayalanath	λ	1299.02		1301.09	nm			
Center Wavelength	^	1303.54		1305.63	nm			
		1309.09		1310.19				
Side-mode Suppression ratio	SMSR	30	-	-	dB			
Average Optical Power, each Lane	Pavg	-4.3	-	+4.5	dBm			
Extinction Ratio	ER	4	-	-	dB			
Transmitter Fue Mark Definition	Complied with IEEE802.3 Clause 88							
Transmitter Eye Mask Definition	100Gbase-LR4							
Average Launch Power OFF	Doff			20	dD.m.			
Transmitter, each Lane	Poff	-	-	-30	dBm			
		Receiver						
		1294.53		1296.59				
Contar Wayalanath	,	1299.02		1301.09				
Center Wavelength	λ	1303.54		1305.63	nm			
		1309.09		1310.19				
Average Power at Receiver Input,		-10.6		4 5	dBm			
each Lane		-10.0	<u>-</u>	4.5	UDIII			
Receiver Sensitivity each Lane				-8.6	dBm			
Receiver Reflectance		-	-	-12	dB			
Los Assert	LosA	-25	-	-	dBm			

Los Dessert	LosD	-	-	-13	dBm
Los Hysteresis	LosH	0.5	-	-	dB

Pin Descriptions





Top Side Viewed from Top

Bottom Side Viewed from Bottom

Pin Function Definitions

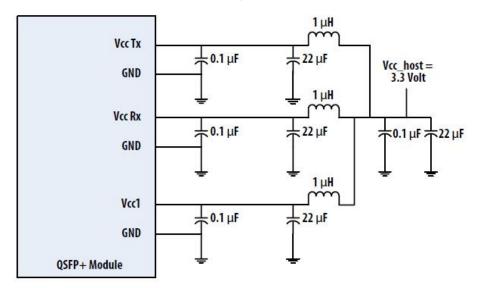
PIN	Logic	Symbol	Name/Description	Note
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data output	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data output	
7		GND	Ground	1
8	LVTLL-I	ModSelL	Module Select	
9	LVTLL-I	ResetL	Module Reset	
10		VccRx	+ 3.3V Power Supply Receiver	2
11	LVCMOS-I/O	SCL	2-Wire Serial Interface Clock	
12	LVCMOS-I/O	SDA	2-Wire Serial Interface Data	
13		GND	Ground	
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	

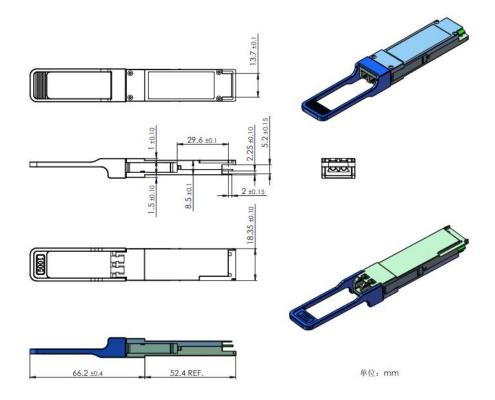
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	1
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3 V Power Supply transmitter	2
30		Vcc1	+3.3 V Power Supply	2
31	LVTTL-I	LPMode	Low Power Mode	
32		GND	Ground	1
33	CML-I	Тх3р	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Output	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Output	
38		GND	Ground	1

Note1. GND is the symbol for signal and supply (power) common for QSFP modules. All are common within the QSFP module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.

Note2. VccRx, Vcc1 and VccTx are the receiver and transmitter power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

Recommended Host Board Power Supply Circuit





Eye Safety

These transceivers are Class 1 laser products. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

Obtaining Document

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