100G QSFP28 SR4

Datasheet



Features:

- 4 independent full-duplex channels
- Up to 25.78125Gbps data rate per channel
- MTP/MPO optical connector
- QSFP MSA compliant
- Digital diagnostic capabilities
- Capable of over 100m transmission on OM4 multi-mode fiber
- Single +3.3V power supply
- Operating case temperature: 0~70C
- Low power consumption < 3.5W
- RoHS-6 compliant

Applications:

• IEEE802.3bm 100GBASE SR4 Ethernet

Product Description

The TQS-HGM1-85DCR is a parallel 100Gbps Quad Small Form-factor Pluggable (QSFP) optical module. It provides increased port density and total system cost savings. The QSFP28 full-duplex optical module offers 4 independent transmit and receive channels, each capable of 25.78125Gbps operation for an aggregate data rate of 100Gbps over 100 meters of OM4 multi-mode fiber.

An optical fiber cable with an MPO/MTP connector can be plugged into the QSFP28 module receptacle. The module operates by a single +3.3V power supply. LVCMOS/LVTTL global control signals, such as Module Present, Reset, Interrupt and Low Power Mode, are available with the modules. A 2-wire serial interface is available to send and receive more complex control signals, and to receive digital diagnostic information.

The TQS-HGM1-85DCR is designed with form factor, optical/electrical connection and digital diagnostic interface according to the QSFP Multi-Source Agreement (SFF-8436). It has been designed to meet the harshest external operating conditions including temperature, humidity and EMI interference. The module offers very high functionality and feature integration, accessible via a two-wire serial interface.

Ordering information

| Part No. | Data Rate | Laser | Fiber Type | Distance* ^{Note1} | Optical Interface | Temp. * ^{Note2} | DDMI |
|----------------|--------------|-------------|---------------|----------------------------|----------------------|--------------------------|------|
| TQS-HGM1-85DCR | 100Gbps | 850nm-VCSEL | MMF | 100m | MPO | ST | Y |

Note1: 100m with 50/125µm OM4 MMF Note2: ST: -5 ~ +70 deg C

Regulatory Compliance

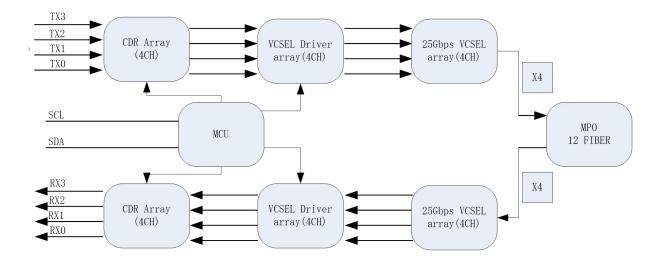
| Feature | Standard | Performance |
|-------------------------|-------------------------------|--|
| Electrostatic Discharge | MIL-STD-883G | HBM class 1, 1000volts and above, |
| (ESD) to the | Method 3015.7 | Contact discharge on Golden Finger. |
| Electrical Pins | | |
| Electrostatic Discharge | IEC-61000-4-2 | Compliant with standards. |
| to the enclosure | GR-1089-CORE | |
| Electromagnetic | FCC Part 15 Class B | Compliant with standards Noise |
| Interference (EMI) | EN55022:2006 | frequency range: 30MHz to 18 GHz. |
| | VCCI Class B | System margins depend on customer |
| | | host board and chassis design. |
| Immunity | IEC 61000-4-3 | Compliant with standards. |
| Laser Eye Safety | FDA 21CFR 1040.10 and 1040.11 | CDRH compliant and Class I laser |
| | EN (IEC) 60825-1:2007 | product. |
| | EN (IEC) 60825-2:2004+A1 | |
| Component Recognition | UL and CUL | Compliant with standards. |
| | EN60950-1:2006 | |
| RoHS6 | 2002/95/EC 4.1&4.2 | Compliant with standards* ^{note3} |
| | 2005/747/EC 5&7&13 | |

Note3:

In light of item 5 in RoHS exemption list of RoHS Directive 2002/95/EC, Item 5: Lead in glass of cathode ray tubes, electronic components and fluorescent tubes.

In light of item 13 in RoHS exemption list of RoHS Directive 2005/747/EC, Item 13: Lead and cadmium in optical and filter glass. The three exemptions are being concerned for Trixon transceivers, because Trixon transceivers use glass, which may contain Pb, for components such as lenses, windows, isolators, and other electronic components.

Block Diagram



Absolute Maximum Ratings*_{Note4}

| Parameter | Symbol | Min | Max | Unit | |
|--------------------------------------|---------|------|---------|------|--|
| Storage Temperature | Tst | -20 | 85 | degC | |
| Relative Humidity (non-condensation) | RH | - | 85 | % | |
| Supply Voltage | VCC | -0.5 | 3.6 | V | |
| Voltage on LVTTL Input | Vilvttl | -0.5 | VCC+0.5 | V | |
| LVTTL Output Current | Lolvttl | - | 15 | mA | |
| Voltage on Open Collector Output | Voco | 0 | 6 | V | |

Note4: Exceeding any one of these values may destroy the device permanently.

Recommended Operating Conditions

| Parameter | Symbol | Min | Typical | Max | Unit | Notes |
|--------------------------------|--------|-----|---------|-----|------|-------|
| Operating Case Temperature | Торс | -5 | - | 70 | °C | |
| Power Supply Voltage | VCC | 3.1 | - | 3.5 | V | |
| Fiber Length: 2000 MHz·km 50µm | | 0.5 | | 70 | ~ | |
| MMF (OM3) | | 0.5 | - | 70 | m | |
| Fiber Length: 4700 MHz·km 50µm | | 0.5 | | 100 | m | |
| MMF (OM4) | | 0.5 | - | 100 | m | |

Performance Specifications – Electrical

| Parameter | Symbol | Min | Тур. | Max | Unit | Notes |
|---------------------------|--------|-----|----------|------|------|-------|
| Data Rate, each Lane | | _ | 25.78125 | _ | Gbps | |
| Power Consumption (XLPPI) | | - | - | 3.5 | W | |
| Supply Current | ICC | - | - | 1.1 | А | |
| Control I/O Voltage, High | VIH | 2.0 | - | VCC | V | |
| Control I/O Voltage, Low | VIL | 0 | - | 0.8 | V | |
| Tx Input Diff Voltage | VI | 300 | - | 1100 | mV | |
| Tx Input Diff Impedance | ZIN | 90 | 100 | 110 | Ω | |

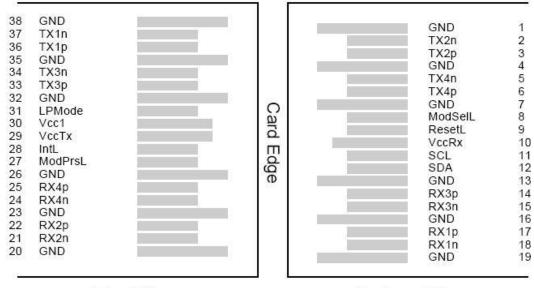
| Rx Output Diff Voltage | Vo | 300 | | 800 | mV | |
|--------------------------|------|-----|-----|-----|----|--|
| Rx Output Diff Impedance | ZOUT | 90 | 100 | 110 | Ω | |

Performance Specifications – Optical

| Parameter | Symbol | Min | Тур. | Max | Unit | |
|---|---------------------------|-------------|------|------|------|--|
| | ٢ | Fransmitter | | | | |
| Center Wavelength | λt | 840 | 850 | 860 | nm | |
| RMS Spectral Width | Λλt | - | - | 0.6 | nm | |
| Average Optical Power, each Lane | Pavg | -8.4 | - | +2.4 | dBm | |
| Optical Modulation Amplitude (OMA) | Poma | -6.4 | - | +3 | dBm | |
| TDEC, each Lane | | - | - | 4.3 | dB | |
| Extinction Ratio | ER | 2 | - | - | dB | |
| Transmitter Eye Mask Definition | Complied with IEEE802.3bm | | | | | |
| Average Launch Power OFF Transmitter, each Lane | Poff | - | - | -30 | dBm | |
| | • | Receiver | | | | |
| Center Wavelength | ٨r | 840 | 850 | 860 | nm | |
| Average Power at Receiver Input, each Lane ^{Note 5} | | -10.3 | - | 2.4 | dBm | |
| Receiver Reflectance | | - | - | -12 | dB | |
| Los Assert | LosA | -30 | - | - | dBm | |
| Los Dessert | LosD | - | - | -10 | dBm | |
| Los Hysteresis | LosH | 0.5 | - | 4 | dB | |

Note 5: Measured with a PRBS31 test pattern, @25.78Gb/s, BER<5E-5.

Pin Descriptions



Top Side Viewed from Top

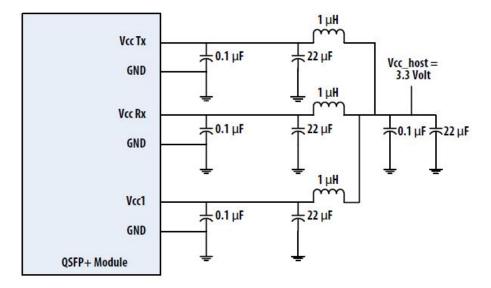
Bottom Side Viewed from Bottom

Pin Function Definitions

| PIN | N Logic Symbol Name/Description | | Note | |
|-----|---------------------------------|---------|--------------------------------------|---|
| 1 | | GND | Ground | 6 |
| 2 | CML-I | Tx2n | Transmitter Inverted Data Input | |
| 3 | CML-I | Tx2p | Transmitter Non-Inverted Data output | |
| 4 | | GND | Ground | 6 |
| 5 | CML-I | Tx4n | Transmitter Inverted Data Input | |
| 6 | CML-I | Tx4p | Transmitter Non-Inverted Data output | |
| 7 | | GND | Ground | 6 |
| 8 | LVTLL-I | ModSelL | Module Select | |
| 9 | LVTLL-I | ResetL | Module Reset | |
| 10 | | VccRx | + 3.3V Power Supply Receiver | 7 |
| 11 | LVCMOS-I/O | SCL | 2-Wire Serial Interface Clock | |
| 12 | LVCMOS-I/O | SDA | 2-Wire Serial Interface Data | |
| 13 | | GND | Ground | |
| 14 | CML-O | Rx3p | Receiver Non-Inverted Data Output | |
| 15 | CML-O | Rx3n | Receiver Inverted Data Output | |
| 16 | | GND | Ground | 6 |
| 17 | CML-O | Rx1p | Receiver Non-Inverted Data Output | |
| 18 | CML-O | Rx1n | Receiver Inverted Data Output | |
| 19 | | GND | Ground | 6 |
| 20 | | GND | Ground | 6 |
| 21 | CML-O | Rx2n | Receiver Inverted Data Output | |
| 22 | CML-O | Rx2p | Receiver Non-Inverted Data Output | |
| 23 | | GND | Ground | 6 |
| 24 | CML-O | Rx4n | Receiver Inverted Data Output | 6 |
| 25 | CML-O | Rx4p | Receiver Non-Inverted Data Output | |
| 26 | | GND | Ground | 6 |
| 27 | LVTTL-O | ModPrsL | Module Present | |
| 28 | LVTTL-O | IntL | Interrupt | |
| 29 | | VccTx | +3.3 V Power Supply transmitter | 7 |
| 30 | | Vcc1 | +3.3 V Power Supply | 7 |
| 31 | LVTTL-I | LPMode | Low Power Mode | |
| 32 | | GND | Ground | 6 |
| 33 | CML-I | Tx3p | Transmitter Non-Inverted Data Input | |
| 34 | CML-I | Tx3n | Transmitter Inverted Data Output | |
| 35 | | GND | Ground 6 | |
| 36 | CML-I | Tx1p | Transmitter Non-Inverted Data Input | |
| 37 | CML-I | Tx1n | Transmitter Inverted Data Output | |
| 38 | | GND | Ground | 6 |

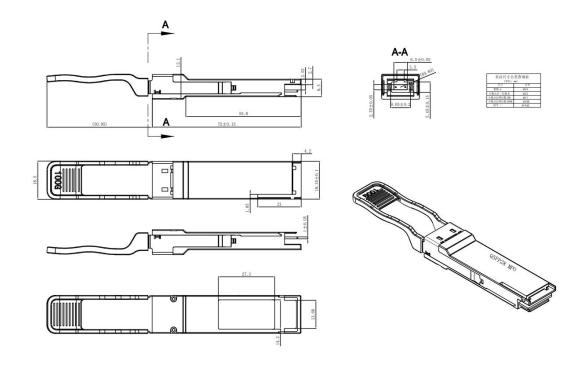
Note6. GND is the symbol for signal and supply (power) common for QSFP modules. All are common within the QSFP module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.

Note7. VccRx, Vcc1 and VccTx are the receiver and transmitter power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.



Recommended Host Board Power Supply Circuit

Mechanical Dimension



Eye Safety

These transceivers are Class 1 laser products. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

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Add: # 202, Section A, Building 1 No.209, Sanse Road, Jinjiang District Industry Park Chengdu 610063. Sichuan P.R. CHINA Tel: (+86) 028-85925400/Fax: (+86) 028-85925445 E-mail: info@trixontech.com http://www.trixontech.com © Copyright Trixon 2014 All rights reserved.

